

PATENT NUMBER

U.S. UTILITY Patent Application.

O.I.P.E.		PATENT DATE
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SCANNED	O.A.	

APPLICATION NO. 09/715748	CONT/PRIOR F	CLASS 438	SUBCLASS 237	ART UNIT 2842 2516	EXAMINER Baron
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Method of forming a diode for integration with a semiconductor device and method of forming a transistor device having an integrated diode

PTO-2040
12/89[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
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